PCN Number:			20140325000						PCN Date:		/27/2104
Title: TPS7B67xx-0			 λ1 datasheet								
Customer Contact:			PCN Manager Phone: +1(214) 480-6037					7	Dept:	Quality	Services
Cha	nge	Туре:									
Assembly Site				Design				Wafer Bump Site			
	Assembly Process			□ Data Sheet □				Wafer Bump Material			
	Assembly Materials				Part number change				Wafer Bump Process		
	Mechanical Specification				Test Site				Wafer Fab Site		
Packing/Shipping/Lab			Labeling	ling Test Process					Wafer Fab Materials		
									Wafer Fa	ab Proc	ess
					PC	N Detail	S				
		tion of Chang uct datasheet(
The following change history provides further details TPS7B6701-Q1, TPS7B6733-Q1 TPS7B6750-Q1, TPS7B6750B-Q1 TEXAS INSTRUMENTS											
		om Revision A (No		n Pa	vicion l	2				W	ww.ti.com Page
			•								rage
	Updated the first page by making the following additions: the <i>Device Information</i> table, device family name to document title, and added the navigation buttons										
• C	Changed the I _Q value from < 2 to < 4 when EN = Low in the <i>Features</i> list							1			
• A	Added the Table of Contents and moved the Revision History to the second page										
	Replaced the ORDERING INFORMATION table with the Device Comparison Table and deleted the Device and Package columns										
• C	Changed KTT (B) terminal number for the ADJ terminal from 4 to — and the number for KTT (ADJ) from — to 4								5		
• A	Added Moved all electrical specifications tables and the <i>Typical Characteristics</i> section into the <i>Specifications</i> section										
• C	Changed the max value for DELAY from V _I to 45 V in the <i>Absolute Maximum Ratings</i> table. Also added new table note for DELAY										
	Changed the max value for ADJ, RESET from V _o to 22 V in the <i>Absolute Maximum Ratings</i> table										
- M	Moved the ESD ratings (now V _{ESD}) and storage temperature into the <i>Handling Ratings</i> table. Added three ESD table notes										
• C	Changed the value of I _O from 1 mA to 450 mA for the Input voltage test conditions in the <i>Electrical Characteristics</i> table										
• A	Added the value for V ₁ in the test conditions of the Regulated output and the Line regulator parameters in the Electrical Characteristics table										
	Moved the timing parameters (TIMING FOR RESET) out of the <i>Electrical Characteristics</i> table and into the new <i>Timing Requirements</i> table							8			
	Added the Overview section title to the first paragraph of the Detailed Description section										
• U	the dated the Revision Co. Record (RECET) and the formation the City in the contract of the contract that V								o the		
• C	rhanged the junction temperature value that disables thermal protection from 170°C to 175°C in the <i>Thermal trotection</i> section										
	Added the Device Functional Modes section										

Revision History (continued)

- Added the Typical Application section in the new Applications and Implementation section
 Added the Power Supply Recommendations section

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS7B67xx-Q1	SLVSCB2A	SLVSCB2B

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/tps7b6701-q1

http://www.ti.com/product/tps7b6733-q1

http://www.ti.com/product/tps7b6750-q1

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS7B6701QPWPRQ1 TPS7B6733QPWPRQ1	TPS7B6750QPWPRQ1
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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USA	PCNAmericasContact@list.ti.com
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