



P-Channel 60 V (D-S) MOSFET



PRODUCT SUMMARY			
V _{DS} (V)	-60		
$R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V	0.015		
$R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V	0.020		
I _D (A) ^d	-50		
Configuration	Single		

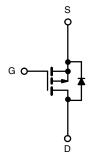
FEATURES

- TrenchFET® power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

· Load switch



P-Channel MOSFET

ORDERING INFORMATION			
Package	TO-252		
Lead (Pb)-free and halogen-free	SUD50P06-15-GE3		
Lead (FD)-lifee and halogen-lifee	SUD50P06-15-T4-GE3		

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	-60	V
Gate-source voltage		V_{GS}	± 20	V
Continuous drain current (T _J = 175 °C)	T _C = 25 °C	I _D	-50 ^d	A
	T _C = 125 °C		-27.5	
Pulsed drain current		I _{DM}	-80	
Avalanche current		I _{AS}	-50	
Single pulse avalanche energy ^a	L = 0.1 mH	E _{AS}	125	mJ
Power dissipation	T _C = 25 °C	В	113 °	W
rower dissipation	T _A = 25 °C	P_{D}	2.5 b, c	l vv
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-ambient ^b	t ≤ 10 s	R _{thJA}	15	18	°C/W
Junction-to-ambient	Steady state		40	50	
Junction-to-case		R _{thJC}	0.82	1.1	

Notes

- a. Duty cycle ≤ 1%
- b. When mounted on 1" square PCB (FR4 material)
- c. See SOA curve for voltage derating
- d. Package limited

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static				•		
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	V
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1	=	-3	V
Gate-body leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
		$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V, T _J = 125 °C	-	-	-50	μΑ
		V _{DS} = -60 V, V _{GS} = 0 V, T _J = 150 °C	-	-	-100	
On-state drain current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-50	-	-	Α
		V _{GS} = -10 V, I _D = -17 A	-	0.012	0.015	
Dynin actives an etata vasiatanas 3		V _{GS} = -10 V, I _D = -50 A, T _J = 125 °C	-	-	0.025	0
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = -10 V, I _D = -50 A, T _J = 150 °C	-	-	0.028	Ω
		V _{GS} = -4.5 V, I _D = -14 A	-	-	0.020	
Forward transconductance ^a	9 _{fs}	V _{DS} = -15 V, I _D = -17 A	-	61	-	S
Dynamic ^b						
Input capacitance	C _{iss}		-	4950	-	
Output capacitance	Coss	V _{GS} = 0 V, V _{DS} = -25 V, f = 1 MHz	-	480	-	pF
Reverse transfer capacitance	C _{rss}		-	405	-	
Total gate charge ^c	Qg		-	110	165	
Gate-source charge ^c	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -50 \text{ A}$	-	19	-	nC
Gate-drain charge ^c	Q_{gd}		-	28	-	
Turn-on delay time ^c	t _{d(on)}		-	15	23	
Rise time ^c	t _r	$V_{DD} = -30 \text{ V}, R_{L} = 0.6 \Omega$	-	70	105	no
Turn-off delay time ^c	t _{d(off)}	$I_D \cong -50 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$	-	175	260	ns
Fall time ^c	t _f		-	175	260	
Source-Drain Diode Ratings and Ch	aracteristics	Г _С = 25 °С ^b				
Continuous current	Is		-	=	-50	Α
Pulsed current	I _{SM}		-	-	-80	A
Forward voltage ^a	V _{SD}	I _F = -50 A, V _{GS} = 0 V	-	-1	-1.6	V
	1	I _F = -50 A, di/dt = 100 A/μs		45	1	

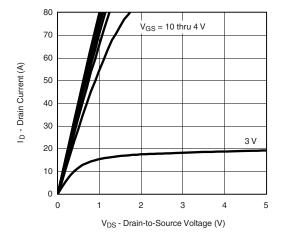
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

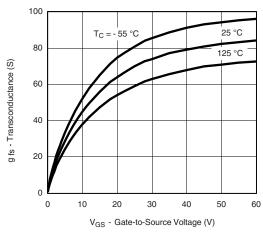
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



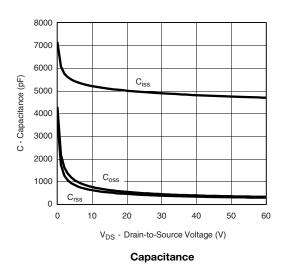
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

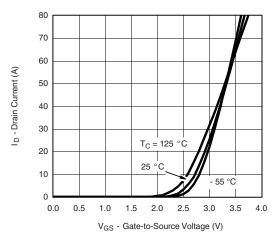


Output Characteristics

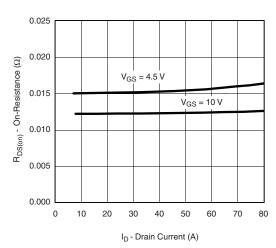


Transconductance

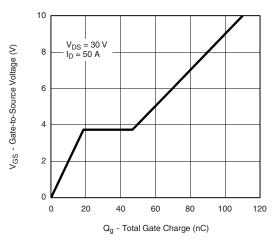




Transfer Characteristics



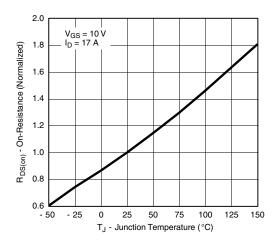
On-Resistance vs. Drain Current



Gate Charge



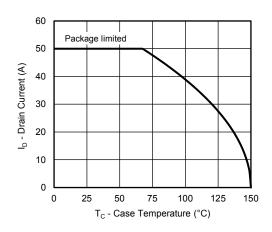
TYPICAL CHARACTERISTICS



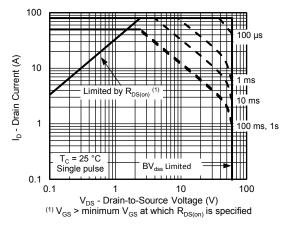
On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage

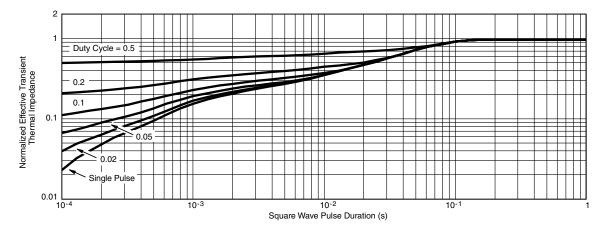
THERMAL RATINGS (25 °C, unless otherwise noted)



Drain Current vs. Case Temperature



Safe Operating Area



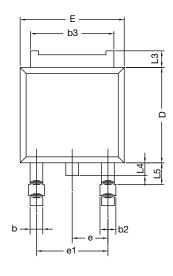
Normalized Thermal Transient Impedance, Junction-to-Case

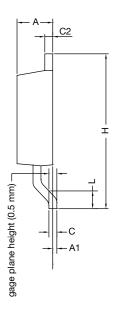
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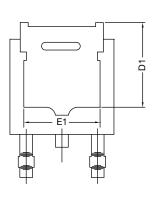


TO-252AA Case Outline

VERSION 1: FACILITY CODE = Y







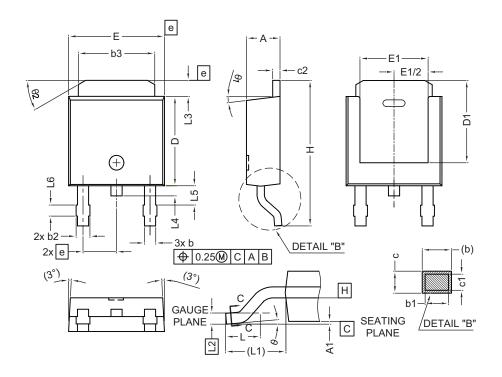
	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
Е	6.35	6.73	
E1	4.32	-	
Н	9.40	10.41	
е	2.28	BSC	
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

Note

• Dimension L3 is for reference only



VERSION 2: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	=	
Е	6.35	6.73	
E1	4.32	=	
е	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74	ref.	
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

Notes

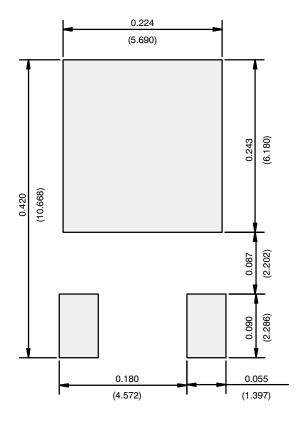
- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022

DWG: 5347



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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