

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	175mΩ @ V _{GS} = 4.5V	1.6A
20V	240mΩ @ V _{GS} = 2.5V	1.3A
	360mΩ @ VGS = 1.8V	1.1A

Description

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

Load Switch









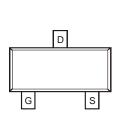
Top View

Features

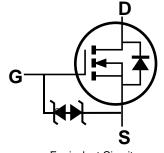
- Low Gate Threshold Voltage
- · Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminals Connections: See Diagram Below
- · Weight: 0.008 grams (Approximate)



Top View Pin-Out



Equivalent Circuit

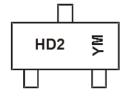
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2310U-7	SOT23	3000/Tape & Reel
DMN2310U-13	SOT23	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



 $\begin{array}{l} \text{HD2} = \text{Product Type Marking Code} \\ \overline{Y}\text{M or YM} = \text{Date Code Marking} \\ \overline{Y} \text{ or Y} = \text{Year (ex: G} = 2019) \\ \text{M} = \text{Month (ex: 9} = \text{September)} \end{array}$

Date Code Key

Year	2019	20	20	2021	2022	20	23	2024	2025	20	26	2027
Code	G	ŀ	1	I	J	-	<	L	M	1	٧	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	20 V		
Gate-Source Voltage	Vgss	±8	V	
Continuous Drain Current (Note 6) V _{GS} = 4.5V	lo	1.6 1.2	А	
Maximum Continuous Body Diode Forward Curren	Is	0.82	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	I _{DM}	4.8	Α	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.48	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	260	°C/W
Total Power Dissipation (Note 6)	·	PD	0.68	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	184	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

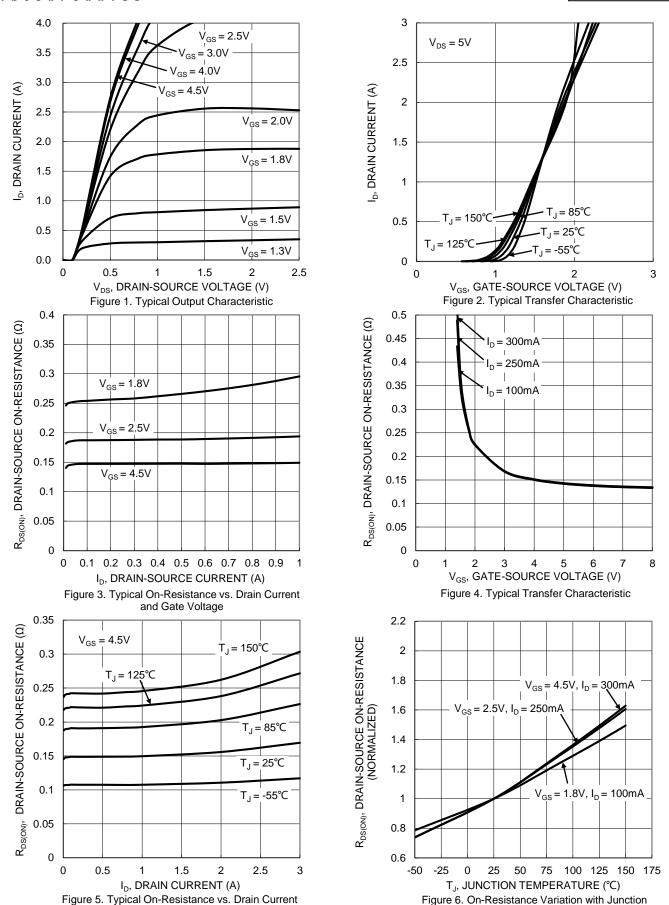
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	-	_	1.0	μΑ	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	10	μA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.45	_	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			140	175		$V_{GS} = 4.5V, I_{D} = 300mA$
Static Drain-Source On-Resistance	RDS(ON)	_	180	240	mΩ	V _{GS} = 2.5V, I _D = 250mA
			245	360		V _{GS} = 1.8V, I _D = 100mA
Diode Forward Voltage	VsD	_	0.8	1.2	V	V _G S = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		38	_	pF	.,, .,
Output Capacitance	Coss		10	_	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss		6	_	pF	1 - 1.51/11/2
Total Gate Charge	Qg	_	0.7	_	nC), 45), 40),
Gate-Source Charge	Q _{gs}	_	0.1	_	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 6A
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	ID = 6A
Turn-On Delay Time	t _{D(ON)}	_	8	_	ns	
Turn-On Rise Time	t _R	_	138	_	ns	$V_{DD} = 10V, V_{GS} = 5V,$
Turn-Off Delay Time	tD(OFF)	_	154	_	ns	$R_L = 1.7\Omega$, $R_G = 6\Omega$
Turn-Off Fall Time	tF		180	_	ns	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.

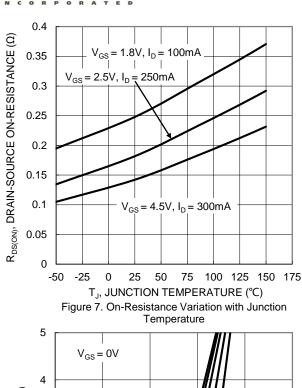


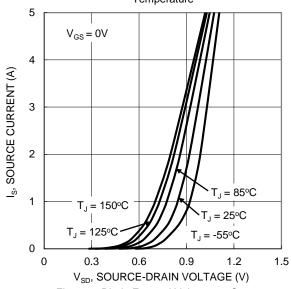


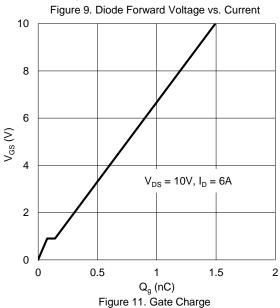
and Junction Temperature

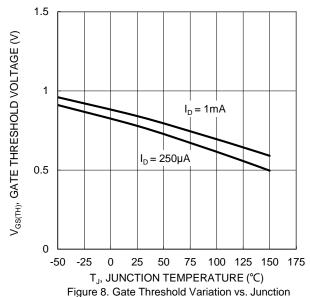
Temperature

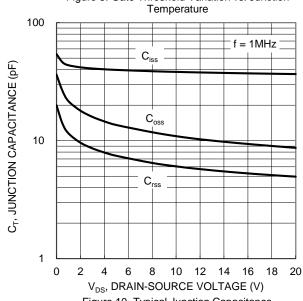


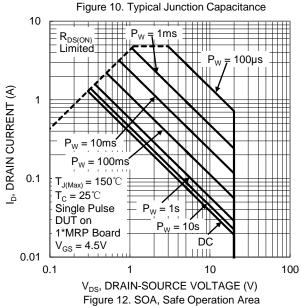














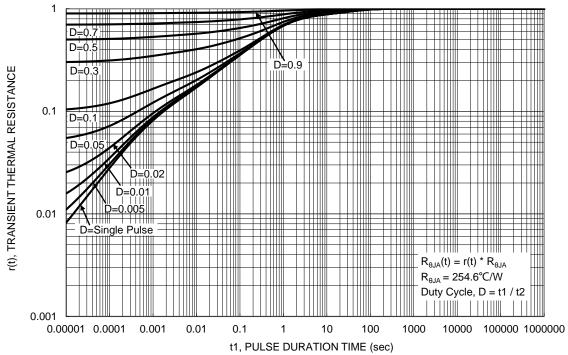


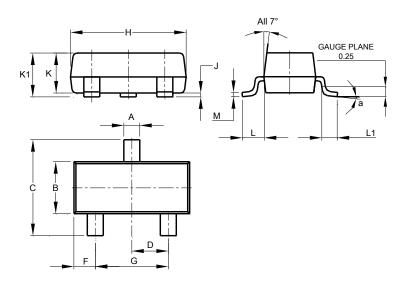
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

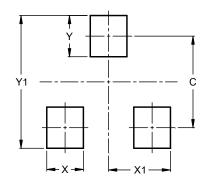


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
a 0° 8°							
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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